



Media Lab Asia

innovating for Digital Inclusion

(A Section 25 Company of Department of Electronics and Information Technology,
Ministry of Communications and Information Technology, Govt. of India)
CIN : U72900MH2001NPL133410
ISO 9001 - 2008 Certified Organisation

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PhD Cell

Ref. No. : PhD-MLA/4(23)/2014-15

Date: 02.02.2017

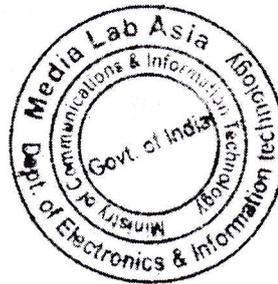
Implementation Order

Subject: Revised Implementation Order for execution of "Visvesvaraya PhD Scheme for Electronics and IT" at Goa University.

Implementing Agency: Goa University.

Please refer to the Implementation order No. PhD-MLA/4(23)/2014 dated 21.10.2014, conveying the approval of the Competent Authority for implementation of a project under the "Visvesvaraya PhD Scheme for Electronics and IT", Goa University, starting from Academic Year 2014-15 batch, with an estimated budget outlay of Rs. 47.18 Lakhs (Rupees Forty Seven Lakhs and Eighteen Thousand only) for duration of the scheme of 60 months for 02 full time PhD candidates.

2. The undersigned is now, directed to convey the revision of the outlay from Rs. 47.18 Lakhs to Rs. 63.884 Lakhs (Rupees Sixty Three Lakhs Eighty Eight Thousand and Four Hundred only) by way of Grant in aid from Media Lab Asia. The revision is due to enhancement of Fellowship and Reimbursement of Rent (RoR) w.e.f. 01/09/2015. The details of the implementation of the scheme are given in the Annexure attached.



(George Arakal)

Director (Admin.& Finance)

To,
The Registrar,
Goa University,
Taleigao, Goa -403206

Copy to:

1. Dr. Rajendra S. Gad, Associate Professor-Department of Electronics, Goa University, Goa-403206
2. Shri. Sanjiv Mittal, JS, MeitY, New Delhi
3. Dr. M. R. Anand, MD & CEO, Media Lab Asia, New Delhi
4. Shri. V. K. Bhatia, Senior Director (Research), Media Lab Asia
5. Ms. Geeta Tripathi, Senior Consultant, PhD Cell, Media Lab Asia
6. Shri. K P Sivadas, Manager Accounts, Media Lab Asia
7. Project File
8. O/o Director (Admin. & Finance)

Annexure to the Revised Implementation Order No. PhD-MLA/4(23)/2014-15 Dated 02.02.2017

1.	Name of the Scheme / Project:	“Visvesvaraya PhD Scheme for Electronics and IT” at Goa University.					
2.	Objectives of the Scheme / Project	The project at Goa University, is a part of the PhD Scheme to enhance the number of PhDs in the Electronic System Design and Manufacturing (ESDM) and IT/IT Enabled Services (ITES) sector.					
3.	Area of coverage	ESDM and IT/ITES					
4.	Name of the Implementing Agency	Goa University.					
5.	Status	State Government Funded University.					
6.	Location/Address	Goa University, Taleigao Plateau, Goa, Goa - 403206.					
7.	No. of PhD Candidates	02 full time					
8.	Full-Time	02 full time					
	Part-Time	---					
9.	Total Scheme / Project duration:	60 months					
10.	Total Scheme / Project Outlay						
	A Year wise and item wise break up of expenditure	(Rupees in Lakhs)					
Sr. No.	Budget Head	1st Yr	2nd Yr	3rd Yr	4th Yr	5th Yr	Total
1.	Fellowship @ Rs. 31,500/- pm (1st & 2nd year) and Rs. 35,000/- pm 3rd year onwards	7.560	7.560	8.400	8.400	8.400	40.320
2.	Part Time fellowship @ Rs.2,50,000/- (One Time incentive)	-	-	-	-	-	-
3.	Reimbursement of Rent	1.512	1.512	1.680	1.680	1.680	8.064
4.	Institutional Overhead@ Rs. 25,000/- per year per Full Time candidate	0.500	0.500	0.500	0.500	0.500	2.500
5.	Annual Contingency grant @ Rs. 30,000/- per year per Full Time candidate	0.600	0.600	0.600	0.600	0.600	3.000
6.	Infrastructural grant @ 50% of total (Up to Rs. 5.0 Lakh per Full Time candidate)	-	10.000	-	-	-	10.000
7.	Young Faculty Research Fellowship @ Rs. 20,000/- per month	-	-	-	-	-	-
8.	Research / Contingency Grant to Young Faculty	-	-	-	-	-	-
	Total:	10.172	20.172	11.180	11.180	11.180	63.884

