



# Media Lab Asia

(A Section 25 Company of Department of Electronics and Information Technology,  
Ministry of Communications and Information Technology, Govt. of India)

Corporate & Registered Office :  
No.2, 4th Floor,  
Samruddhi Venture Park,  
Central M.I.D.C. Road  
Andheri (East), Mumbai - 400093  
Tel.: +91-22-28312930 / 28312931  
Fax: +91-22-28379158

## PhD Cell

Ref. No. : PhD-MLA/4(23)/2014

Date: 21.10.2014

### Implementation Order

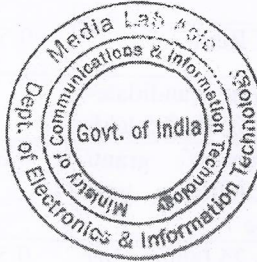
**Subject: Implementation Order for execution of "Visvesvaraya PhD Scheme for Electronics and IT" at Goa University.**

**Implementing Agency: Goa University, Goa**

The undersigned is directed to convey the approval of the Competent Authority for implementation of the "Visvesvaraya PhD Scheme for Electronics and IT", at Goa University, Goa.

2. The estimated budget outlay of your institution for the PhD Scheme for 02 full time PhD candidates is Rs. 47.18 Lakhs (Rupees Forty Seven Lacs and Eighteen Thousand only) by way of Grant in aid from Media Lab Asia. The duration of the scheme shall be 60 months. The details of the implementation of the scheme are given in the Annexure attached.

3. The release of funds shall be subject to receiving the duly signed copy of the terms & conditions by Media Lab Asia.



(George Arakal)  
Director (Admin. & Finance)

To  
The Director,  
Goa University,  
Gao-403 206

Copy to:

1. Dr. Rajindra S. Gad, Associate Professor, Deptt. of Electronics, Goa University
2. Dr. Ajay Kumar, JS, DeitY, New Delhi
3. Mr. Arun Sachdeva, MD & CEO, Media Lab Asia, New Delhi
4. Mr. K.P. Sivadas, Manager Accounts, Media Lab Asia
5. Project File
6. O/o Director (Admin. & Finance)



## Annexure to the Implementation Order No. PhD-MLA/4(23)/2014, Dated 21.10.2014

1.	Name of the Scheme / Project:	“Visvesvaraya PhD Scheme for Electronics and IT” at Goa University, Goa					
2.	Objectives of the Scheme / Project	The project is a part of the PhD scheme “to enhance the number of PhDs in the Electronic System Design and Manufacturing (ESDM) and IT/IT Enabled Services (ITES) sectors”. Under this project Goa University will carry out R&D in System in System on chip solutions for MIMO systems modelling and performance studies.					
3.	Area of coverage	ESDM and IT/ITES					
4.	Name of the Implementing Agency	Goa University, Goa					
5.	Status	Central Government					
6.	Location/Address	Taleigao Plateau, Goa					
7.	No. of PhD Candidates	02 full time					
8.	Full-Time	02 full time					
	Part-Time	--					
9.	Total Scheme / Project duration:	60 months					
10.	<b>Total Scheme / Project Outlay</b> <b>A Year wise and item wise break up of expenditure</b> (Rupees in Lakhs)						
Sr. No.	Budget Head	1 <sup>st</sup> Yr	2 <sup>nd</sup> Yr	3 <sup>rd</sup> Yr	4 <sup>th</sup> Yr	5 <sup>th</sup> Yr	Total
1	Fellowship @ Rs.22,500/-pm (1 <sup>st</sup> & 2 <sup>nd</sup> year) Rs.25,000/-pm 3 <sup>rd</sup> year and onwards	5.40	5.40	6.00	6.00	6.00	28.80
2	Reimbursement of Rent	0.54	0.54	0.60	0.60	0.60	2.88
3.	Support to part-time candidate-one time incentive @ Rs.2.5 lakh	-	-	-	-	-	-
4.	Annual contingency grant/support @Rs.30,000/-per year full time Candidate	0.60	0.60	0.60	0.60	0.60	3.00
5.	Overheads @Rs.25,000/- per year per full time candidate	0.50	0.50	0.50	0.50	0.50	2.50
6.	Infrastructural Grant @ 50% of total subject to Rs.5.0 lakh per full time candidate	-	10.00	-	-	-	10.00
	<b>Total:</b>	<b>7.04</b>	<b>17.04</b>	<b>7.70</b>	<b>7.70</b>	<b>7.70</b>	<b>47.18</b>

