

<b>D 3.19</b>	<p><b>Minutes of the Meeting of Board of Studies in Portuguese held on 20/04/2018.</b> The Academic Council did not approve the minutes of the meeting of the Board of Studies in Portuguese held on 20/04/2018.</p> <p>The Chairperson, Board of Studies was requested to take the matter back to the Board of Studies and then place the same before the Academic Council for approval.</p> <p style="text-align: center;"><b>(Action: AR-PG)</b></p>
<b>D 3.20</b>	<p><b>Minutes of the meeting of Board of Studies in Journalism and Mass Communication held on 12/03/2018 and reconvened meeting on 22/03/2018.</b> The Academic Council approved the minutes of the meeting of the Board of Studies in Journalism and Mass Communication held on 12/03/2018 and the reconvened meeting held on 22/03/2018.</p> <p style="text-align: center;"><b>(Action: AR-PG)</b></p>
<b>D 3.21</b>	<p><b>Minutes of the meeting of Board of Studies in Physics held on 12/04/2018.</b> The Academic Council approved the minutes of the meeting of the Board of Studies in Physics held on 12/04/2018.</p> <p style="text-align: center;"><b>(Action: AR-PG)</b></p>
<b>D 3.22</b>	<p><b>Minutes of the meeting of Board of Studies in Electronics held on 19th April 2018.</b> The Academic Council approved the minutes of the Board of Studies in Electronics held on 19th April 2018.</p> <p style="text-align: center;"><b>(Action: AR-PG)</b></p>
<b>D 3.23</b>	<p><b>Minutes of the meeting of Board of Studies in History - PG held on 26<sup>th</sup> April 2018.</b> The Academic Council approved the minutes of the meeting of the Board of Studies in History- PG held on 26<sup>th</sup> April, 2018 with the following suggestions:</p> <ol style="list-style-type: none"> <li>1. Course structure to be submitted as an annexure and not in the minutes.</li> <li>2. The Syllabus for the Goa University Admissions Ranking Test (GU-ART) for 2018-19 to be based on current Goa University TY Syllabus (3 Units)</li> </ol> <p style="text-align: center;"><b>(Action: AR-PG)</b></p>
<b>D 3.24</b>	<p><b>Minutes of the meeting of Board of Studies in Sociology held on 27<sup>th</sup> March 2018.</b> The Academic Council approved the minutes of the meeting of the Board of Studies in Sociology held on 27<sup>th</sup> March 2018 with the following suggestions:</p> <ol style="list-style-type: none"> <li>1. Compulsory Courses to be changed to Core Courses.</li> <li>2. Course codes to be corrected.</li> </ol> <p style="text-align: center;"><b>(Action: AR-PG)</b></p>
<b>D 3.25</b>	<p><b>Minutes of the meeting of Board of Studies in Hindi held on 28/03/2018.</b> The Academic Council approved the minutes of the meeting of the Board of Studies in Hindi held on 28/03/2018. The Chairperson, Board of Studies was requested to indicate the number of hours instead of lectures in the Syllabus.</p> <p style="text-align: center;"><b>(Action: AR-PG)</b></p>
<b>D 3.26</b>	<p><b>Minutes of the meeting of Board of Studies in Marathi held on 23rd &amp; 26th April,</b></p>

	<p><b>Part-E</b></p> <p>(i) Recommendations of text books for the courses of study at the under-graduate level.</p> <p>Non-agenda item.</p> <p>(ii) Recommendations of text books for the courses of study at the post-graduate level.</p> <p>List of Text books and reference books are given course wise at the end of the syllabus in Appendix B.</p> <p><b>Part-F</b></p> <p>(i) The declaration by the Chairman that the minutes were read out by the Chairman at the meeting itself.</p> <p>Date: 12<sup>th</sup> April 2018</p> <p style="text-align: right;"><b>(Dr. Ramesh V. Pai)</b> <b>Signature of the Chairman</b></p> <p>Place: Goa University</p> <p><b>Part-G</b></p> <p>Remarks of the Dean</p> <p>(i) The minutes are in order</p> <p>(ii) May be recommended for approval of Academic Council</p> <p>(iii) Special remarks if any</p> <p>Date:</p> <p>Place: Goa University</p> <p style="text-align: right;"><b>(Prof. Gaurish M.Naik)</b> <b>Signature of the Dean</b></p> <p style="text-align: right;"><a href="#">(Back to Index)</a></p>
D 3.22	<p><b>Minutes of the meeting of Board of Studies in Electronics held on 19<sup>th</sup> April 2018.</b></p> <p><b>Part A</b> <b>(LXI) Recommendations regarding courses of study in the subject or group of subjects at the undergraduate level:</b></p>

	<p>NA</p> <p><b>(II) Recommendations regarding courses or group of subjects at postgraduate level:</b> New set of SWAYAM &amp; Flipped classroom Courses are added and also the Restructuring of credits are done as per the recent directive. <a href="#">Annexure I</a> (refer page no 1074)</p> <p><b>Part B</b></p> <p><b>(LXII) Scheme of the Examinations at Undergraduate Level:</b></p> <p>No change</p> <p><b>(II) Panel of examiners for different examinations at Undergraduate Level:</b> No change</p> <p><b>(III) Scheme of the examinations at post-graduate level:</b> No change</p> <p><b>(IV) Panel of examiners for different examinations at post-graduate Level:</b> No Change</p> <p><b>Part C</b></p> <p><b>(LXIII) Recommendations regarding preparation and publication and selection of Anthologies in any subject or group of subjects and the names of person recommended for appointment to make the selection</b> Nil</p> <p><b>Part D</b></p> <p><b>(LXIV) Recommendations regarding general academic requirements in the Departments of University or affiliated colleges:</b></p> <p>No change</p> <p><b>II) Recommendation of Academic Audit Committee and status thereof</b> No recommendations received so far by BoS electronics from AAC</p> <p><b>Part E</b></p> <p><b>(LXV) Recommendations of text books for the course for study at the Undergraduate level:</b></p> <p>No change</p> <p><b>(II) Recommendations of text books for the courses of study at the post Graduate level:</b> List of books required is indicated below each subject in the syllabus.</p> <p><b>Part F</b></p>
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	<p><b>Important points for consideration/approval of Academic Council</b></p> <p>(i) The Important points/recommended of BOS that require consideration/approval of Academic council (points to be highlighted) as mentioned below.</p> <p>Revision of PG syllabus as per Academic council Directive to make 16 credits per semester and 12 hours per credit Added objectives and learning outcomes for PG Papers</p> <p>(II) The declaration by the Chairman, that the minutes were read out by the Chairman at the meeting itself.</p> <p style="text-align: right;"><b>Prof. Gaurish M. Naik</b> s/d <b>Signature of Chairman</b></p> <p>Date: Place: Goa University</p> <p><b>Part G: The remarks of the Dean of the Faculty.</b></p> <p>(I) The minutes are in order. (II) The minutes may be placed before the Academic Council with remarks if any. (III) May be recommended for approval of Academic Council (IV) Special remarks if any</p> <p style="text-align: right;"><b>Prof. Gaurish M. Naik</b> s/d <b>Signature of the Dean</b></p> <p>Date: Place: Goa University</p> <p style="text-align: right;"><a href="#">(Back to Index)</a></p>
D 3.23	<p><b>Minutes of the meeting of Board of Studies in History – PG held on 26<sup>th</sup> April 2018.</b></p> <p><b>Part A.</b></p> <p>vii. Recommendations regarding courses of study in the subject or group of subjects at the undergraduate level: NIL.</p> <p>viii. Recommendations regarding courses of study in the subject or group of subjects at the postgraduate level:</p> <p>The BoS has recommended the following Core and Optional Courses for the Masters in History programme w.e.f. the academic year, 2018-2019: <a href="#">Annexure I (refer page no 1105)</a> <b>Core courses:</b> HSC-150 : “Doing History”: An Introduction to Historical Methods</p>

**D 3.22 Minutes of the meeting of Board of Studies in Electronics held on 19<sup>th</sup> April 2018.**

**Annexure I**

**ANNEXTURE-I**

**SYLLABUS OF M.SC. (ELECTRONICS) (Effective from AY: 2018-19)**

The Course requirement is completion of 64 credits (ie 16 credits/semester,

SR. NO	COURSE CODE	TITLE	CREDITS	TYPE
<b>SEMESTER-I</b>				
1	ELC101	MICROELECTRONICS AND VLSI DESIGN	4	L
2	ELD101	ADVANCED DIGITAL COMMUNICATION SYSTEMS	4	L
3	ELD102	NUMERICAL COMPUTATION AND ALGORITHMS( <b>FLIPPED CLASSROOM</b> )	4	L
4	ELC102	ELECTRONICS PRACTICALS – I	4	P
5	ELD 181	<b>SWAYAM-I</b> ( Digital Communication)	4	L
TOTAL			16	
<b>SEMESTER II</b>				
1	ELC 201	EMBEDDED SYSTEMS DESIGNS and IoT( <b>FLIPPED CLASSROOM</b> )	4	L
2	ELD 201	OPTICAL COMMUNICATION SYSTEMS	4	L
3	ELD 202	OPERATING SYSTEM AND RTOS	4	L
4	ELC 202	ELECTRONICS PRACTICALS- II	4	P
5	ELD 281	<b>SWAYAM-II</b> ( Optical Communication)	4	L
6	ELD 203	BASICS OF MEDICAL IMAGING	1	L
TOTAL			16	
<b>SEMESTER III</b>				

1	ELC 301	SIGNALS AND SYSTEMS	4	L
2	ELD 301	DIGITAL SIGNAL PROCESSING	4	L
3	ELD 302	INSTRUMENTATION & CONTROL THEORY	4	L
4	ELC302	ELECTRONICS PRACTICALS – III	4	P
5	ELD 381	<b>SWAYAM-III</b> ( Data analytics)	4	L
6	ELD 303	DIGITAL SYSTEM DESIGN USING HDL	4	L
7	ELD 304	EDA TOOLS ( <b>FLIPPED CLASSROOM</b> )	4	L
8	ELD 305	INDUSTRIAL INTERNSHIP	1	L+P
			TOTAL	16
<b>SEMESTER IV</b>				
1	ELD 401	PROJECT	8	P
2	ELC 401	LASER SYSTEM ENGINEERING	4	L
3	ELC 402	ELECTRONICS PRACTICALS – IV	4	P
4	ELD 481	<b>SWAYAM-IV</b> ( Wireless Sensors Network/Data Analytics Programming)	4	L
5	ELD 402	NANOELECTRONICS & NANOSYSTEMS	4	L
6	ELD 403	PHARMACEUTICAL INSTRUMENTATION	4	L
7	ELD 404	COMMUNICATION AND TECHNICAL SKILLS ( <b>FLIPPED CLASSROOM</b> )	4	T+P
			TOTAL	16

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**Programme:** M. Sc. (Electronics)

#### **SEMESTER I**

**Course Code:** ELC101 **Title of the Course:** MICROELECTRONICS AND VLSI DESIGN

**Number of Credits:** 4

<b><u>Prerequisites for the course:</u></b>	Should have graduate level knowledge in analog and digital electronics	
<b><u>Objective:</u></b>	This subject will introduce to the VLSI Technology , various fabrications processes involved in IC design , Electrical and Electronics analysis of few circuits, Some Design examples of VLSI circuits, Circuit Optimization techniques, Advance circuits designs examples of Memory, Registers, Synchronous circuits etc.	
<b><u>Content:</u></b>	An overview of VLSI, Modern CMOS Technology	4
	Silicon Logic, Logic design with MOSFET.	5

	Physical structure of CMOS Integrated circuits	4
	Fabrication Technologies of CMOS Integrated Circuits	7
	Elements of Physical Design	3
	Electrical characteristics of MOSFETS	6
	Electronic analysis of CMOS Logic gates	5
	Advanced Techniques in CMOS Logic Circuits	6
	System specifications using HDL, General VLSI components	4
	Memories and Programmable Logic	4
	Tutorials: 1. 2 <sup>nd</sup> order Butterworth filter using P-Spice student version. 2. Current Mirrors using P-Spice student version. 3. CMOS based Op-Amp using P-Spice student version. 4. Study of Lithography. 5. Compares various Static memories.	
	Total	48
<b><u>Pedagogy:</u></b>	Lectures/ tutorials/assignments/self-study	
<b><u>References/Readings</u></b>	1. Introduction to VLSI Circuits and Systems, John P. Uyemura, WILEY. 2. Principles of CMOS VLSI Design, N.H.E. W. & Eshahiraghian, Addison Wesley 3. Modern VLSI Design System on Silicon, Pearson Education Asia. By W. Wolf. 4. VLSI Technology, S.M. Sze, McGraw-Hill (1995). 5. Basic VLSI Design, Douglas Pucknell, K. Eshraghian, Prentice Hall India.	
<b><u>Learning Outcomes</u></b>	Students should be able to design fundamental gates and customize them for specific electrical and electronics application, Should understand the fabrication processes involved in VLSI technology, Write the Hardware descriptive form of circuits, Synchronize the combinational and sequential circuits, design a static and dynamic memory cell, Understand the Programmable logic building blocks.	

**Course Code:** ELD101

**Title of the Course:** ADVANCED DIGITAL COMMUNICATION SYSTEMS

**Number of Credits:** 4

<b><u>Prerequisites for the course:</u></b>	Graduate level understanding in basics of Electronic Communications	
<b><u>Objective:</u></b>	This course is intended to introduce to students into the basics of wireless systems – concepts, theory. It covers various modulation techniques, to enable the student to synthesize and analyze wireless and mobile cellular communication systems over a stochastic fading channel	
<b><u>Content:</u></b>	<b>Introduction to Mobile and Cellular Communication Systems:</b> Main Definitions, impact of Mobile and Cellular Radio Communication Historical overview. Fundamental of Radio Mobile and Cellular Practices Radio mobile links and cells, Frequency re-use, Principles of Cellular Com. Mobile Telephone Switching Subsystem, The mobile frequency spectrum, Hand-off, Cochannel and adjacent channel interference limitations, Near-far problem, Power Control.	6
	<b>Mobile Communication Channel including antennas:</b> The mobile wireless propagation channel, Notions on antennas especially the near and far field concept, Line of Sight (LOS) propagation, Multipath fading , outdoor and Indoor Propagation, Flat and selective fading, Special antennas for base stations and headsets, Deterministic, Empirical and Statistical Methods for propagation link computations.	8
	<b>Overview of Mobile and Cellular Radio Communication Modulation and Detection Techniques:</b> Analog modulations and detection: AM, FM, PM, ACSB, Hybrid and Digital modulation: PCM, ASK, FSK, QPSK, QAM, MSK, etc, Coherent and noncoherent detection, C/N, S/N, Eb/No and BER relations, Probability concepts, Mobile Radio links parameters.	10
	<b>Overview of Multiple Accesses Techniques:</b> Simplex, Duplex TDD and Time Division Duplex, Time division multiple access (TDMA) FDMA and OFDM, Code Division multiple access (CDMA), Hybrid multiple access, Management of voice, Data and Video (Multimedia) information.	09
	<b>Modern Digital Radio Systems:</b> standards, proposals and comparisons GSM (Europe and all over the world) –	



	<p>TDMA, IS-54 (U.S.A.)- TDMA, IS-95 (U.S.A., Korea) CDMA-, PHS (Japan) – TDMA, Frequency Hopping (FH) (U.S.A.) – CDMA, PCS, PCS Cordless telephone 2<sup>nd</sup> generation (CT-2), Cellular digital packet data (CDPD), and Wireless LAN, New standard trends Edge, 3<sup>rd</sup> and 4<sup>th</sup> generation beginning, LTE,</p> <p><b>Mitigation Techniques for Mobile System:</b> Overview of Natural and manmade external noise sources, Radiation hazards effects from base stations, Mobile and portable equipments.</p> <p><b>Diversity Techniques for Mobile Radio Systems:</b> Dispersive channels, Space diversity, Frequency diversity, Equalizer techniques</p> <p><b>Tutorials:</b></p> <ol style="list-style-type: none"> <li>1. Study of Global Positioning system working principle.</li> <li>2. Study of mobile Service providers in Goa Region.</li> <li>3. Study of AIR station Bambolim, Goa.</li> <li>4. Study of Distance Education Infrastructure Setup (DEITE) at Goa University.</li> <li>5. Study of various interfacing of mobile set eg. Bluetooth.</li> </ol>	<p>07</p> <p>04</p> <p>04</p> <hr/>
	Total	48
<b><u>Pedagogy:</u></b>	lectures/ tutorials/assignments	
<b><u>References/Readings</u></b>	<p>(CMLX) Steele, R., Hanzo, L., “Mobile Radio Communication” 3<sup>rd</sup> Edition Wiley 2005.</p> <p><b>2. Rappaport, T.S., “Wireless Communications: Principles And Practice, 2/E, Pearson</b></p> <p>3. Wireless Communications (WIRELESS COMMUNICATIONS, 2<sup>ND</sup> ED, Molisch A F),Wiley</p>	
<b><u>Learning Outcomes</u></b>	<p>At the end of the course,</p> <ol style="list-style-type: none"> <li>1.the students will be able to understand the design, specifications and the performances of various wireless communication systems</li> <li>2. Apply the cellular concepts to evaluate the signal reception performance in a cellular network.</li> <li>3. Apply the traffic analysis to design cellular network with given quality of service constraints.</li> <li>4. Determine the appropriate model of wireless fading channel based on the system parameters and the</li> </ol>	

	property of the wireless medium. 5. Analyze and design receiver and transmitter diversity techniques.	
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**Course Code:** ELD102 **Title of the Course:** Numerical Computation and Algorithms  
**Number of Credits:** 4

<b><u>Prerequisites for the course:</u></b>	Students should have a knowledge of programming	
<b><u>Objective:</u></b>	The primary objective of the course is to develop the basic understanding of numerical algorithms and skills to implement algorithms to solve mathematical problems on the computer and also Data Bases.	
<b><u>Content:</u></b>	<b>Computer Programming:</b> Introduction to Algorithms, Elements of Computer Programming language Basics of algorithm design, general model, Dynamic programming model, principle of optimality, backtracking models. <ul style="list-style-type: none"> <li>• Algorithm order and complexity.</li> <li>• Backtracking example.</li> </ul>	08 hours
	<b>Data Structures:</b> Introduction to Data Structures, Vectors and Lists, Binary Trees, Graphs, Hashing. <ul style="list-style-type: none"> <li>• Implementation of Shortest path algorithm</li> <li>• Implementation of binary tree</li> </ul>	10 hours
	<b>Theory of Numerical programming:</b> Theory of numerical errors, Numerical Integration: Trapezoidal & Simpsons rule, Romberg method, Improper integrals; Numerical Solution of linear equations: Guass-Jordon elimination and Lu decomposition, Numerical Solutions of nonlinear equations: Bracketting, bisection, Secant & Regular falsi method, Newton-Raphson method; Numerical Solutions to Ordinary differential equations: Runge-Kutta method, Modified midpoint method, Richardson extrapolation. <ul style="list-style-type: none"> <li>• Trapezoid methods, Newtons Raphson methods</li> <li>• Bisection and Regular falsi methods</li> <li>• Runge Kutta</li> </ul>	24 hours
	<b>Database:</b> Basic Concepts, Relational Data Model, Database Design, DBMS storage structures and access methods, Query Processing, Transaction Processing, Security & Integrity, Distributed Databases, Client Server Computing. <ul style="list-style-type: none"> <li>• SQL for database</li> <li>• Client Server data base query</li> </ul>	06 hours

	<b>Tutorials:</b> <ol style="list-style-type: none"> <li>1. Implementation of Vector in C++.</li> <li>2. Implementation of List in C++.</li> <li>3. Implementation of minimum path algorithms in C++.</li> <li>4. Simple Example of Database querying in C++.</li> <li>1. Case study on the Emerging Trends in databases (Data mining).</li> </ol>	
	Total	48
<b><u>Pedagogy:</u></b>	lectures/ tutorials/presentation/practical	
<b><u>References/Readings</u></b>	<ol style="list-style-type: none"> <li>2. Data structures using C and C++ by Yedidyah Langsam, Moshe J Augenstein, Aaron M Tenenbaum, Prentice Hall of India, 1995</li> <li>3. Data Abstraction and Problem solving in Java by Frank M Carrano, Janet J Prichard ,Addison-Wesley, 2001</li> <li>4. Numerical Recipes in C, William H. Press, Brian P. Flannery, William T. Vetterling, Saul A. Teulosky, Cambridge University Press, 1990.</li> <li>5. Numerical Mathematical Analysis, J. B. Scarborough, Oxford and IBM Publishing Company (1979).</li> <li>6. Numerical Recipes in C: The Art of Scientific Computing by William H Press, Brian P Flannery, Saul A Teukolsky – Mathematics – 1992.</li> <li>7. Fundamentals of Database Systems, 4<sup>th</sup> Edition by R Elmasri, S Navathe Addison-Wesley, 2003</li> </ol>	
<b><u>Learning Outcomes</u></b>	After completing this course they will be able to use numerical methods for solving a problem, locate and use good mathematical software, get the accuracy you need from the computer, assess the reliability of the numerical results, and determine the effect of round off error or loss of significance. Solve a linear system of equations using an appropriate numerical method	

**Course Code:** ELC 102    **Title of the Course:** ELECTRONICS PRACTICALS –I  
**Number of Credits:** 4

<b><u>Prerequisites for the course:</u></b>	Should have studied graduate level basic level electronic subject. It is assumed that students have a working knowledge of passive and active components and digital circuits.	
<b><u>Objective:</u></b>	The hardware experiments give a student hands-on experience to design the basic digital and analog circuits, usually found in house hold appliances. The simulations experiments give understanding of the digital communications having various modulation techniques and also data correction and detection in general communication system.	

<b><u>Content:</u></b>	<p>Hardware experiments</p> <ol style="list-style-type: none"> <li>1. Design of variable voltage supply @ 2 Amps.</li> <li>2. Temperature Controller using 741.</li> <li>3. Design of Function Generator.</li> <li>4. Design of 4-bit UP-DOWN Counter.</li> <li>5. Design of Power Amplifier 10 Watts.</li> <li>6. Design of Stepper driver using Monoshot &amp; 555 Timer.</li> </ol> <p>Software Simulations</p> <ol style="list-style-type: none"> <li>7. Implementation of MSK modulation and demodulation.</li> <li>8. ASK, FSK, QPSK, modulation &amp; demodulation.</li> <li>9. QPSK, modulation &amp; demodulation</li> <li>10. DS-CDMA simulation.</li> <li>11. Channel Coding methods. <ol style="list-style-type: none"> <li>a. Convolution</li> <li>b. Block code</li> </ol> </li> <li>12. Error detection and correction Algorithm <ol style="list-style-type: none"> <li>a. CRC</li> <li>b. Hamming code</li> </ol> </li> </ol>	
	Total	96
<b><u>Pedagogy:</u></b>	Presentations /assignments/self-study	
<b><u>Learning Outcomes</u></b>	The student will understand and should be able to handle basic equipment in house hold. Also, he will thoroughly understand the basics of communication system for modulation, data coding , error coding channel coding methods.	

**Course Code:** ELD181

**Title of the Course:** SWAYAM-I

**Prerequisite/objectives/learning outcomes as provided by course on SWAYAM website.**

**Number of Credits:** 4

## SEMESTER II

**Course Code:** ELC 201

**Title of the Course:** EMBEDDED SYSTEMS DESIGNS & IoT

**Number of Credits:** 4

<b><u>Prerequisites for the course:</u></b>	Should have studied microprocessor and C programming at graduate level	
<b><u>Objective:</u></b>	<ul style="list-style-type: none"> <li>Architectures of Microcontroller and its programming with Interfacing various Interfaces is discussed in depth in this paper.</li> <li>In this course students are going to learn how to develop apps for Android phone using SDK.</li> <li>To Understand the Architectural Overview of IoT</li> <li>To Understand the IoT Reference Architecture and Real-world Design Constraints</li> </ul>	

	<ul style="list-style-type: none"> <li>To Understand the various IoT Protocols ( Data link, Network, Transport, Session, Service)</li> </ul>	
<b><u>Content:</u></b>	<p><b>Architectures: Embedded system , Computer Architecture,</b> RISC/CISC and Harvard/Princeton Architectures, Introduction to 8-bit Micro controllers , ARM : Introduction to 32/64-bit Processors, Latest ARM ,ARM Architecture &amp; Organization, ARM/THUMB, ARM/THUMB Instruction Set, ARM Exception Handling, Timers/Counters, UART, SPI, PWM, WDT, Input Capture, Output Compare Modes, I2C .</p> <p><b>Interfacing:</b> LED, Switches, ADC, DAC, LCD</p> <p><b>Programming :</b> ARM programming in Assembly and C (GNU Tools),</p> <p><b>Introduction to Android &amp; app development</b></p> <p><b>IoT ARCHITECTURE AND PROTOCOLS:</b></p> <p>IoT-An Architectural Overview– Building an architecture, Main design principles and needed capabilities, An IoT architecture outline, standards considerations. M2M and IoT Technology Fundamentals- Devices and gateways, Local and wide area networking, Data management, Business processes in IoT, Everything as a Service (XaaS), M2M .</p> <p>Introduction IoT Big Data Analytics</p> <p><b>IOT DATA LINK LAYER &amp; NETWORK LAYER PROTOCOLS</b></p> <p>PHY/MAC Layer(3GPP MTC, IEEE 802.11, IEEE 802.15), Wireless HART,Z-Wave, Bluetooth Low Energy, Zigbee Smart Energy, DASH7 – Network Layer-Ipv4, Ipv6, 6LoWPAN, 6TiSCH,ND, DHCP, ICMP, RPL, CORPL, CARP</p> <p><b>TRANSPORT &amp; SESSION LAYER PROTOCOLS</b></p> <p>Transport Layer (TCP, MPTCP, UDP, DCCP, SCTP)-(TLS, DTLS) – Session Layer-HTTP, CoAP, XMPP, AMQP, MQTT</p> <p><b>SERVICE LAYER PROTOCOLS &amp; SECURITY</b></p> <p>Service Layer –oneM2M, ETSI M2M, OMA, BBF – Security in IoT Protocols– MAC 802.15.4 , 6LoWPAN, RPL, Application Layer.</p>	<p><b>10</b></p> <p><b>2</b></p> <p><b>7</b></p> <p><b>3</b></p> <p><b>8</b></p> <p><b>2</b></p> <p><b>5</b></p> <p><b>6</b></p> <p><b>5</b></p>
<b><u>Total</u></b>		<b>48</b>
<b><u>Pedagogy:</u></b>	lectures/ tutorials/assignments/self-study/Flipped classroom	

<b><u>References/Readings</u></b>	<ol style="list-style-type: none"> <li>1. Jivan Parab et al., Exploring C for microcontroller ( Springer 2007)</li> <li>2. Lipovski G. J. Single and multiple Chip Microcontroller interfacing. Prentice Hall, USA 1998.</li> <li>3. Beginning Android 4 Application Development</li> <li>4. Professional Android 4 Application Development</li> </ol> <p>Learning Android Game Programming : A Hands-On Guide to Building Your First Android Game 1<sup>st</sup> Edition</p> <p>5 .Jan Holler, VlasiosTsiatsis, Catherine Mulligan, Stefan Avesand,StamatisKarnouskos, David Boyle, "From Machine-to-Machine to theInternet of Things: Introduction to a New Age of Intelligence", 1<sup>st</sup> Edition, Academic Press, 2014.</p> <p>8. Bernd Scholz-Reiter, Florian Michahelles, "Architecting the Internet ofThings", ISBN 978-3-642-19156-5 e-ISBN 978-3-642-19157-2, Springer</p> <p>9. Vijay Madiseti and ArshdeepBahga, "Internet of Things (A Hands-onApproach)", 1<sup>st</sup> Edition, VPT, 2014.</p>	
<b><u>Learning Outcomes</u></b>	<ul style="list-style-type: none"> <li>• Students will be able to develop their own embedded platform using ARM</li> <li>• They will be able to design android application for mobiles</li> <li>• understand where the <b>IoT</b>concept fits and possible future trends; understand the various network protocols used in Application</li> </ul>	

**Course Code:** ELD201    **Title of the Course:** OPTICAL COMMUNICATION SYSTEMS  
**Number of Credits:** 4

<b><u>Prerequisites for the course:</u></b>	The Knowledge of Electro statics and 1136atheterization. Also, basic understanding of analog and digital communication is preferable.	
<b><u>Objective:</u></b>	The paper highlights importance of optical communication over existing copper cable and microwave communication. It also gives an elaborate view of electromagnetic spectrum usage for various applications starting from telephony till satellite communication. A strong theoretical base is created to understand the difference between ray theory and wave theory approach for passage of signal in optical fibers. The estimation of noise in optical detection is discussed in detail. The paper emphasizes the industrial needs in cabling technique and type of cable used. Different	

	techniques of optical fiber manufacturing and measuring their characteristic are discussed.	
<b>Content:</b>	<p><b>Light Propagation in Optical Fiber:</b> Geometric picture, Pulse spread due to material dispersion, loss mechanism, Theory of Optical waveguides, methods of waveguides analyses , modes in steps and graded index fiber, new types of optical fibers</p> <p><b>Fiber Optics Technology:</b> Glass fiber fabrication, cable design, coupling, splicing and connectors, splicing methods, connectors, fiber measurements.</p> <p><b>Optical Sources:</b> LED and LDs, development of Laser diodes structures, transmitter circuits, Coupling efficiency of source to fiber.</p> <p><b>Optical detectors:</b> Photodiodes, Avalanche diodes and other detectors.</p> <p><b>Receiver sensitivity and BER:</b> Receiver design, Noise in detectors.</p> <p><b>Communication System design:</b> System requirement, System design, Link analyses, Power budgeting.</p> <p><b>Transmission:</b> TDM, Undersea fiber optics communication system , WDM and DWDM techniques</p>	<p>7</p> <p>7</p> <p>6</p> <p>6</p> <p>8</p> <p>7</p> <p>7</p>
	Total	48
<b>Pedagogy:</b>	Lectures/Tutorials/Presentations /self-study	
<b>References/Readings</b>	<ol style="list-style-type: none"> <li>1. Optical Fiber Communication by A. Selvarajan and etal TMH, .</li> <li>2. Optical Fiber Communication by Gerd Keiser , MGH , .</li> <li>3. Optical Electronics, 4<sup>th</sup> Edition by A. Yariv, HRW publication,</li> </ol>	
<b>Learning Outcomes</b>	The students at the end of the paper, will have some knowledge of designing a point to point optical link for a given situation. They will also be able to choose the right type of components if an assignment of optical network design is given. The course is also useful for students who would like to join telecom industries, as many aspects of practical situation are discussed during course of study. They are also taught to monitor signal losses during course of signal transmission. The student from this course will be confident	

**Course Code:** ELD 202    **Title of the Course:** OPERATING SYSTEM AND RTOS  
**Number of Credits:** 4

<b><u>Prerequisites for the course:</u></b>	Should have studied digital electronics at graduate level	
<b><u>Objective:</u></b>	This course develops to focus on concept of highlighting the various methods of improvising speed of computing machine through the operating system organization and various entity managements. Further the subject is developed to analyse the small embedded system developments through the Real Time Operating Systems for task management efficiency.	
<b><u>Content:</u></b>	<p>Introduction to Computer Organization and Architecture : hardware vs. software –the virtual machine concept, concept of von Neumann architecture, hardware components and functions, trends in hardware development, system configurations and classifications.</p> <p>Process Description and Control: Processes, process states, processor modes, context switching, CPU scheduling algorithms, threads.</p> <p>Concurrency Control: Concurrent processes, critical section problem and solutions, mutual exclusion solution requirements, semaphores and monitors.</p> <p>Deadlocks: Characterization, detection and recovery, avoidance, prevention.</p> <p>Inter Process Communication: classical IPC problems and solutions, IPC techniques.</p> <p>The Input/Output and File Subsystem: I/O devices, controllers and channels, bus structures, I/O techniques (programmed, interrupt driven and DMA), I/O subsystem layers. Concepts of files and directories, issues and techniques for efficient storage and access of data. I/O and file system support for graphics, multimedia, databases, transaction processing and networking.</p> <p>The Memory Subsystem: Memory types and hierarchy, module level Organization, cache memory. Memory partitioning, swapping, paging, segmentation, virtual memory.</p> <p>The Central Processing Unit: CPU components, register sets, instruction cycles, addressing</p>	<p><b>(CMLXI)</b> o ur s</p> <p><b>(CMLXII)</b> o ur s</p> <p><b>(CMLXIII)</b> o ur s</p> <p><b>(CMLXIV)</b> o ur s</p> <p><b>(CMLXV)</b> o ur s</p>



	<p>modes, instruction sets, concept of micro-programming ,Basics of RISC approach, pipelined and super-scalar approaches, vector processors and parallel processors, hardware support for the OS.</p> <p>µCOS case study</p> <p>Tutorial</p> <ol style="list-style-type: none"> <li>1. Implementing Lower Level Shell</li> <li>2. Implementing Signal in Unix</li> <li>3. Hard disk partitioning in Linux</li> </ol>	<p>(CMLXV O ur S</p> <p>(CMLXV O ur S</p> <p>(CMLXV O ur S</p> <p>(CMLXI O ur S</p>
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	Total	48
<b><u>Pedagogy:</u></b>	Lectures/ tutorials/assignments/self-study	
<b><u>References/Readings</u></b>	1. Operating system principles, 3 <sup>rd</sup> Edition, by William Stallings – PHI (1998) 2. Operating system concepts by Silberchatz and Galvin - Addison 1140athet 3. Operating system by Tanaumbuam, PHI New Delhi	
<b><u>Learning Outcomes</u></b>	Will able to generalize the understanding of the computing machine and various entities associated with the enhancement of the efficiency. Will able to handle the operating system management process, memory, I/O, Secondary Disk and organizations of various. Students will able to handle any operating system for process and task managements if follows the documentations of the same.	

**Course Code:** ELC 202    **Title of the Course:** ELECTRONICS PRACTICALS-II  
**Number of Credits:** 4

<b><u>Prerequisites for the course:</u></b>	Should have studied microcontrollers and embedded system.	
<b><u>Objective:</u></b>	The students will handle experiments on processor and controllers like 8086, 89C51, PIC and ARM controller derivatives for Input Output operation, Various communication interfaces, data acquisition, task management.	
<b><u>Content:</u></b>	1. Coping the memory segment using 8086 Assembler 2. Sorting of numbers using 8086 Assembler 3. Multiplication & Division using 8086 Assembler 4. LCD & LED Interfacing to ATMEL 89C52 5. 7-segment Interfacing to ATMEL 89C52 (BCD counter) 6. Display Temperature using ATMEL 89C52 7. Serial Transmission and reception PIC16F877 8. Configuring On – chip ADC PIC16F877 9. Waveform generation using I2C based Max5822 interfaced to PIC 16F877 10. Hex Keypad Interfaced to ARM controller 11. LCD & LED Interfacing using ARM controller 12. Switching of tasks using ARM controller a. OS – I using ARM    b. OS – II using ARM 15. Shell programming - Web Application. 16. Shell programming - System Management 17. Shell programming - Data processing	

	Total	96
<b><u>Pedagogy:</u></b>	Presentations /self-study/laboratory design and implementation	
<b><u>Learning Outcomes</u></b>	Should able to analyze the architectures of any processor, controller. Will able to designs some application using embedded system using tasks for real time applications. Should able to handle any computing machine using shell script for computing and management.	

**Course Code:** ELD 282

**Title of the Course:** Swayam-II

**Number of Credits: 4**

**Prerequisite/objectives/learning outcomes as provided by course on SWAYAM website**

**Course Code:** ELD 203

**Title of the Course:** BASICS OF MEDICAL IMAGING

**Number of Credits: 1**

[illegible]

<b><u>References/Readings</u></b>	1. <b>Introduction to Medical Imaging:</b> Physics, Engineering and Clinical Applications ,Cambridge 2. <a href="#">Medical Imaging: Principles and Practices</a> ,CRC press.	
<b><u>Learning Outcomes</u></b>	This course enriches a common man regarding non-invasive techniques used by hospitals and clinics to monitor the various health related issues. The course also prepares a student for higher learning in field of biomedical electronics.	

### SEMESTER III

**Course Code:** ELC 301

**Title of the Course:** Signals and Systems

**Number of Credits:** 4

<b><u>Prerequisites for the course:</u></b>	Should have studied first year of M.Sc electronics	
<b><u>Objective:</u></b>	The objectives of this course are to develop good understanding about signals, systems and their classification; to provide with necessary tools and techniques to analyze electrical networks and systems to develop expertise in time-domain and frequency domain approaches. Also discusses different types of Filters and Its design.	
<b><u>Content:</u></b>	<p><b>1. Signal And Signal Processing:</b> Characterization and classification of signal, Typical signal Operations.</p> <p><b>2.Discrete time signal and Systems:</b> Time Signal, Sequence representation, Sampling process, Simple Interconnection schemes, Correlation of Signal, Random Signal.</p> <p><b>3.Discrete Time Fourier Transform:</b> Continuous Discrete-time FT, Energy Density Spectrum, Phase and Group Delays, Sampling of continuous tie signal, Low pass &amp; Band pass Signal, Anti-Aliasing Filter design, Sample and Hold, A to D, D to A convertors, Effects of sample and hold.</p> <p><b>4.Digital Filter Structure:</b> Block diagram representation, FIR, IIR filter, Allpass filter, Tunable IIR Digital filter, Digital Sin-Cosine generator. Computational complexity.</p> <p><b>5.FIR Digital Filter Design:</b> Preliminary considerations, FIR Design based on windowed FS, Design of minimum phase.</p>	<p>04</p> <p>08</p> <p>10</p> <p>08</p> <p>07</p>

	<p><b>2. DSP Algorithm implementation:</b> Structure simulation, Computation of DFT, DFT &amp; IDFT using MATAB, Sliding DFT, Number representation, Handling overflow, Tunable digital filters.</p> <p><b>3. Application of Digital Signal Processing:</b> Dual tone multi frequency tone signal Detection, Musical sound processing, Signal compression, Trans multiplexers.</p> <p>Tutorials:</p> <ol style="list-style-type: none"> <li>History of Fourier Transform.</li> <li>Understanding Speech Spectral Analysis Problem.</li> <li>Understanding FFT.</li> <li>Study of TMS Series of processors.</li> <li>MATLAB program for generation of complex exponential sequence.</li> </ol>	06  05
<b>Total</b>		<b>48</b>
<b>Pedagogy:</b>	lectures/ tutorials/assignments/self-study/presentation/	
<b>References/Readings</b>	<ol style="list-style-type: none"> <li>Sanjit K Mitra, Digital Signal Processing: A computer Based Approach</li> <li>Digital Signal Processing, Johnny Johnson, PHI.</li> <li>Digital Signal Processing, Proakis, PHI.</li> </ol>	
<b>Learning Outcomes</b>	<p>Applying different signal processing algorithms to any given application.</p> <p>Learns about Different types FIR and IIR filters</p>	

**Course Code:** ELD 301  
**Number of Credits:** 4

**Title of the Course:** Digital Signals Processing

<b>Prerequisites for the course:</b>	Basic knowledge in Numerical Methods and computation at graduate level or higher.	
<b>Objective:</b>	This course develops concepts in designing the experiment in Matlab and Simulink.	
<b>Content:</b>	<p>Students have to design the following experiments in Matlab and Simulink and plot the characteristics of the signal processing system under design.</p> <ol style="list-style-type: none"> <li>Filters <ol style="list-style-type: none"> <li>Lp norm</li> <li>Ensemble averaging Filters</li> <li>Exponential moving average systems</li> <li>Median filter</li> <li>FIR</li> </ol> </li> <li>Understanding and implementation of aliasing effect.</li> </ol>	12  05

	3.Oscillators a. Design using Van der Pol's equation b. Lorentz oscillators systems c. Gaussian oscillators systems  4.FFT and DFT:design and implementation of DFT and FFT based algorithms, and their application in communication.  <b>(CMLXX)</b> Image processing a. Interpolations b. Pattern recognition  using PCA  6.Simulink a. Transfer function design and study for impulse and finite sequence. b. Convolution	07  05  05  09  05
<b>Total</b>		48
<b>Pedagogy:</b>	lectures/ self-study/presentation/lab courses	
<b>Learning Outcomes</b>	<ul style="list-style-type: none"> <li>• Student learn how to use the advanced mathematical tools how to apply them for signal processing.</li> <li>• Student can plot the signals in both time domain and transform domains using MATLAB</li> <li>• Students also learns to uses SIMULINK tool to model his/her design</li> <li>• Learns Image processing algorithms PCA etc.</li> </ul>	

**Course Code:** ELD 302

**Title of the Course:** INSTRUMENTATION & CONTROL THEORY

**Number of Credits:** 4

<b>Prerequisites for the course:</b>	Graduate level knowledge in analog and digital electronics, Basics of differential equations.	
<b>Objective:</b>	<ul style="list-style-type: none"> <li>• Various principles of transduction and actuator are discussed in this course. The important parameters used in instrument characterization are also explained. Types of error committed by a user and how to deal with them are explained with examples. Also, various standards followed for accurate measurement are discussed in depth. The techniques used to convert analog data into digital domain and</li> </ul>	

	its analysis and storage are also discussed in this course. How a PID controller is tuned for a given application is also discussed in this paper. Few important instruments such as Oscilloscope, spectrum analyzers, wave analyzers, Lock in amplifiers are described in depth.	
<b><u>Content:</u></b>	<p><b>Introduction:</b> Basic Concepts of measurements, calibrations and standards. Transducers (Types and parameters) and Sensors: Displacement, strain, vibration, Pressure, Flow, Temperature, Force and Torque (linearity, accuracy, precision, bandwidth, repeatability)</p> <p><b>Amplification:</b> Simple ended, Differential and Instrumentation amplifier.</p> <p><b>Sampling:</b> An Anti-aliasing, Multiplexers, Sample and Hold, Track and Hold.</p> <p><b>Computer Interfaces:</b> Serial (RS-232), Parallel, GPIB (IEEE-488), Universal Serial Bus (USB)</p> <p><b>Display Devices:</b> Review of LED, LCD, CRT devices, segmental and dot matrix displays. <b>General purpose test equipments:</b> CRO, Digital storage oscilloscope, Digital voltmeter, Wave Spectrum analysis, Lock-in-amplifiers, Pulse generators and waveform generators,</p> <p><b>Control System:</b> Types of control system – open loop, closed loop, linear, non-linear, continuous, discrete, frequency and time response, open loop motor control, DC motor phase control, PD, PI, PID</p> <p>Tutorials:</p> <ol style="list-style-type: none"> <li>1. Study of Open loops control System.</li> <li>2. Electronics Chocks.</li> <li>3. Design of On/Off temperature controller using thermistor sensor.</li> <li>4. Study of SEM.</li> <li>5. Study of Scanning Probe technique.</li> </ol>	<p>7</p> <p>5</p> <p>5</p> <p>4</p> <p>7</p> <p>10</p> <p>10</p>
<b><u>Total</u></b>		48
<b><u>Pedagogy:</u></b>	Lectures/Assignment , Presentation	
<b><u>References/Readings</u></b>	<p><b>(CMLXXI)</b> Industrial Control Electronics – John Webb, Kevin</p> <p>Greshok, Merrill Publications, .</p> <p>2. Elements of Electronic Instrumentation and Measurement, Joseph J. Carr, Prentice Hall India.</p>	

	3. Modern Electronic Instrumentation and Measurement Techniques, Albert Helfnick, William Cooper, PHI 4. Instrumentation Measurement by Northrop CRC 2001	
<b><u>Learning Outcomes</u></b>	This course is appropriate for the students who would like to make his career in industries. The features of various networks taught in this course will enable him/her to guide an industry for choosing an appropriate instrumentation network and types of interfaces he can adopt for automation of sophisticated instruments used in quality control and analysis. The course empowers a student who is likely to go for higher studies in electronics and Instrumentation technology.	

**Course Code:** ELC302 **Title of the Course:** Electronics Practical III

**Number of Credits:** 4

<b><u>Prerequisites for the course:</u></b>	Should have knowledge in microcontroller and embedded systems	
<b><u>Objective:</u></b>	The course gives hands on experience on TMS 320 DSP, Altera NIOS II and National Instruments Platform	
<b><u>Content:</u></b>	<ol style="list-style-type: none"> <li>1.Design of S/C circuit for Strain gauge /Glucose strip @ 3.3V.</li> <li>2. Design of S/C circuit for Thermistor sensor @ 3.3 V and interfacing with ARM.</li> <li>3. FFT using TMS 320.</li> <li>4. Convolution using TMS 320.</li> <li>5. Analysis of frequency components using Spectrum Analyzer</li> <li>6. VHDL implementation for the Multiplexer &amp; Demultiplexer</li> <li>7. VHDL Implementation for Encoder &amp; Decoder</li> <li>8. VHDL implementation for the Counter.</li> <li>9. Verilog implementation for the Memory Module.</li> <li>10. Verilog implementation for the Latch.</li> <li>11. Display Hello world and blinking Led's using NiosII soft core</li> <li>12. Matrix Manipulation on NIOSII Core (Multiplication, determinant, Inverse, Transpose)</li> <li>13. Android (two experiments)</li> <li>14. NI ELSVIS(two experiments)</li> <li>15. Obstacle Avoidance using 89V52 based Robot</li> <li>16. Obstacle detection for varying range using 89v52 based Robot</li> <li>17. Line follower using 89v52 based Robot</li> </ol>	
<b><u>Total</u></b>		96
<b><u>Pedagogy:</u></b>	Assignment , Presentation and Laboratory work	
<b><u>Learning Outcomes</u></b>	<ul style="list-style-type: none"> <li>• On completing this course they are in a position to</li> </ul>	



	design signal conditioning circuit, <ul style="list-style-type: none"> <li>• also they are exposed to Altera FPGA by implementing various digital circuits using VHDL and Verilog.</li> <li>• Student themselves will be able to develop an android app.</li> <li>• Can handle a NI ELVIS board to implement and testing any circuit.</li> </ul>	
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**Course Code:** ELD 381( Data analytics)

**Title of the Course:** Swayam-III

**Prerequisite/objectives/learning outcomes as provided by course on SWAYAM website.**

**Number of Credits:** 4

**Course Code:** ELD303

**Title of the Course:** Digital System Design Using HDL

**Number of Credits:** 4

<b><u>Prerequisites for the course:</u></b>	Should have studied digital electronics at graduate level.	
<b><u>Objective:</u></b>	This course develops concepts in Principles of Combination and Sequential logic design, VHDL and Verilog.	
<b><u>Content:</u></b>	<b>1. Introduction:</b> About Digital Design, Analog versus Digital, Electronic Aspects of Digital Design, PLD's, ASIC, Digital Design level. Digital Concept and Number System: General Positional number system conversions, Operation, BCD, Gray Code, Character Codes, Codes for Actions, Conditions, and States n-Cubes and Distance, Codes for Detecting and Correcting Errors, Error-Detecting Codes, Error-Correcting and Multiple-Error-Detecting Codes, Hamming Codes, CRC Codes, Two-Dimensional Codes, Checksum Codes, m-out-of-n Codes, Codes for Serial Data Transmission and Storage, Parallel and Serial Data, Serial Line Codes,	07
	<b>2. Combinational Logic Design Principles:</b> Switching Algebra, Combinational-Circuit Analysis, Combinational-Circuit Synthesis, and Timing Hazards.	08
	<b>3. Hardware Description Languages:</b> HDL-Based Digital Design, ABEL Hardware Description Language, The VHDL Hardware Description Language, The Verilog Hardware Description Language,	06
	<b>4. Combinational Logic Design Practices:</b>	06

	<p>Documentation Standards, Circuit Timing, Combinational PLDs, Decoders, Encoders, Three-State Devices , Multiplexers, Exclusive-OR Gates and Parity Circuits , Comparators, Adders, Subtractors, and ALUs , Combinational Multipliers .</p> <p><b>5. Sequential Logic Design Principles &amp; Practices:</b>  Bistable Elements, Latches and Flip-Flops, Clocked Synchronous State-Machine Analysis, Clocked Synchronous State-Machine Design, Designing State Machines Using State Diagrams, State-Machine Synthesis Using Transition Lists, Another State-Machine Design Example, Decomposing State Machines, Feedback Sequential-Circuit Analysis, Feedback Sequential-Circuit Design, ABEL Sequential-Circuit Design Features ,Sequential-Circuit Design with VHDL , Sequential-Circuit Design with Verilog, Sequential-Circuit Documentation Standards , Latches and Flip-Flops ,Sequential PLDs , Counters, Shift Registers, Iterative versus Sequential Circuits , Synchronous Design Methodology , Impediments to Synchronous Design , Synchronizer Failure and Metastability</p> <p><b>6. Memory, CPLDS, AND FPGAS</b>  Read-Only Memory, Read/Write Memory, Static RAM, Dynamic RAM, Complex Programmable Logic Devices, Field-Programmable Gate Arrays</p> <p><b>Tutorials:</b>  1. Design flow for the simple microprocessor in HDL  2. Study and compares types of RAMS.  3. Design of GRAY code circuit.  4. Study of ALTERA PLD's  5. Study of XYLINX PLD's.  6. Studying WEB Pack Xilinx tool.</p>	<p>09</p> <p>12</p>
<b>Total</b>		48
<b>Pedagogy:</b>	lectures/ tutorials/assignments/self-study	
<b>References/Readings</b>	1. Digital Design Principles and Practices, by John F. Wakerly, Prentice Hall's Fourth Edition. 2. Digital Logic Applications & Designs by John M. Yarbough, CWS Publishing Co. Division of Thomson Learning, 3. Giovanni De Micheli, "Synthesis and Optimization of Digital Circuits," Tata McGraw-Hill, 2003. 4. Srinivas Devadas, Abhijit Ghosh, and Kurt Keutzer,	

	<p>"Logic Synthesis," McGraw-Hill, USA, 1994.</p> <p>5. Neil Weste and K. Eshragian,"Principles of CMOS VLSI Design: A System Perspective,2<sup>nd</sup> edition, Pearson Education, 2000.</p> <p>6. Kevin Skahill, "VHDL for Programmable Logic," Pearson Education, 2000. M.N.O. Sadiku, Elements of Electromagnetics 2<sup>nd</sup> Edition), Oxford University press, 1995.</p>	
<b><u>Learning Outcomes</u></b>	Explains Principles of Combination and Sequential logic design and HDL.	

**Course Code:** ELD 304**Title of the Course:** EDA Tools**Number of Credits:** 4

<b><u>Prerequisites for the course:</u></b>	Should have studied Digital Communication Systems	
<b><u>Objective:</u></b>	This course develops concepts in Programming with different types of EDA Tools	
<b><u>Content:</u></b>	<p>Study of JTAG, Modelsim Syntax study.</p> <ol style="list-style-type: none"> <li>1. Study of Phases of Quartus compilations. 4</li> <li>2. Study of phases of ISE compilations 4</li> <li>3. Testing logic using ChipScope-I. 4</li> <li>4. Testing logic using ChipScope-II 4</li> <li>5. Parallel implementation of CRC. 4</li> <li>6. Serial implementation of CRC. 4</li> <li>7. FIFO implementation 4</li> <li>8. pulse stretcher 4</li> <li>9. Test bench using Modelsim-I 4</li> <li>10. Test bench using Modelsim-I 4</li> <li>11. Test bench using Modelsim-I 4</li> <li>12. Test bench using Modelsim-I 4</li> </ol>	
<b><u>Total</u></b>		48
<b><u>Pedagogy:</u></b>	Assignments/self-study/Lab courses/FLIPPED CLASSROOM	
<b><u>References/Readings</u></b>	<ol style="list-style-type: none"> <li>1. Design through Verilog HDL By T. R&gt; Padmanabhan &amp; Sundari. IEEE press, Wiley Interscience.</li> <li>2. <a href="http://www.xilinx.com/itp/xilinx7/help/iseguide/html/ise_fpga_design_flow_overview.htm">http://www.xilinx.com/itp/xilinx7/help/iseguide/html/ise_fpga_design_flow_overview.htm</a></li> <li>3. Hands on experience on altera development board by J.S.Parab,etal: Springer Netherland 2018(ISBN 978-81-322-3769-3)</li> </ol>	
<b><u>Learning Outcomes</u></b>	The Student will be able to use different types of EDA tools and learn programming with these tools.	

**Course Code:** ELD 305**Title of the Course:** Industrial Internship**Number of Credits:** 1

<b><u>Prerequisites for the course:</u></b>	Should have graduate level knowledge of Electronics	
<b><u>Objective:</u></b>	This course develops concepts in industrial training, preparing seminars and working on short term projects	
<b><u>Content:</u></b>	<b>Industrial training and Seminar:</b> A student has to undergo Industrial training equivalent to one credit for the period of minimum 1 month in the respective Electronics industries / Research Laboratory anywhere in India. Each student has to give a power point presentation on the industrial internship which they had undergone	24
<b><u>Pedagogy:</u></b>	Self-study/presentation	
<b><u>Learning Outcomes</u></b>	6. The Student will be exposed to the different kinds of working environments in electronic industries. 7. Will be able to understand industrial flow and make a documentation.	

#### SEMESTER IV

**Course Code:** ELC 401

**Title of the Course:** Laser System Engineering

**Number of Credits:** 4

<b><u>Prerequisites for the course:</u></b>	Graduate level knowledge in Electronics/Physics	
<b><u>Objective:</u></b>	At the end of the course the student is expected to know the difference between ordinary light and light emitted by a laser device. Which are different method used for excitation of laser devices? Why four level lasers are more efficient as compared to three level? The theory to explain the generation of stimulated emissions. Actual laser systems used in industry and examples most powerful lasers in the world. Application of lasers in medical, civil and defense areas.	
<b><u>Content:</u></b>	<b>Optical Resonators:</b> Energies in resonator, Febry-Perot Etalon , Febry-Perot Etalon as Optical Spectrum Analyzer, Mode Stability Criteria , Resonance Frequency of Optical Resonator, Unstable Resonator <b>Interaction of Radiation with Atomic System:</b> Spontaneous transmission between Atomic layer, Homogenous and In-Homogeneous broadening , Line shape functions, Stimulated transmission , Absorption and amplification , gain saturation in Homogenous media . <b>Theory of Laser Oscillator:</b> Febry Perot Laser , Three and Four Level Laser , Power in Laser	9  8  8

	<p>Oscillator, Optimum Light coupling , Multimode Laser Oscillator and Mode Locking Methods of Mode locking , Pulse length Measurements , Q-Switching , methods of Q-Switching .</p> <p><b>Laser Systems:</b> Pumping and laser Efficiency, Ruby Laser, Flash Pumping ,Nd-YAG Laser , Nd Glass Laser , Threshold for CW and Pulse operation , He-Ne Laser , CO<sub>2</sub> Laser , Ar-Ion Laser , Excimer Laser , Dye Laser.</p> <p><b>Non –Linear Optics:</b> Origins of Non-Linear Polarization, relation between induced Polarization</p> <p><b>Interaction of Light and Sound:</b> Scattering of Light by Sound, RamanNath and Bragg diffraction , Defraction of light by Sound , Intensity modulation .</p> <p><b>Optical Communication:</b> Advances in optical Communication, Optical Network.</p> <p>Tutorials:</p> <ol style="list-style-type: none"> <li>1. Understanding Diffraction of Laser Light using grating</li> <li>2. Comparison of resolving power of Prism and Grating.</li> <li>3. Focusing of Laser Light.</li> <li>4. Collimation of Laser Light.</li> <li>5. Study of Raman Laser system.</li> </ol>	<p>7</p> <p>4</p> <p>6</p> <p>6</p>
<b>Total</b>		48
<b>Pedagogy:</b>	Lectures/presentation/assignments	
<b>References/Readings</b>	<ol style="list-style-type: none"> <li>1. Optical Electronics, 4<sup>th</sup> Edition by A. Yariv, HRW publication, .</li> <li>2. OptoElectronbics , by Ghatak and Tyagarajan TMH Publication .</li> </ol>	
<b>Learning Outcomes</b>	The student has sufficient knowledge of lasers for applications involving medical treatment as well as defense needs. They will have a full knowledge of classification of lasers and its usage. Now a days, most of the industries use high power lasers as a tool, the student with this knowledge will be handy in guiding the work force for safe use of laser.	

**Course Code:** ELC 402

**Title of the Course:** ELECTRONICS PRACTICALS – IV

**Number of Credits: 4**

<b><u>Prerequisites for the course:</u></b>	Should have studied EDA Tools .	
<b><u>Objective:</u></b>	<ul style="list-style-type: none"> <li>The course is intended to introduce to the students with LabVIEW and SPEEDY 33 Boards and MYRio</li> </ul>	

	<p>Bthoard</p> <ul style="list-style-type: none"> <li>Also there are few labs on Altera DE2 Board using NIOS II soft core Processor.</li> </ul>	
<b><u>Content:</u></b>	<ol style="list-style-type: none"> <li>1. Reading from flash using DE2 board</li> <li>2. LCD and 7 segment Interfacing using DE2 board</li> <li>3. PS/2 Mouse Interface on DE2 board</li> <li>4. UART Interface using DE2 board</li> <li>5. Blinking of LEDs using RTOS on DE2 Board.</li> <li>6. KEY pad and ADC interfacing using RTOS</li> <li>7. Echo implementation on speedy33 kit(lab view)</li> <li>8. Reverberation implementation on speedy33 kit(lab view)</li> <li>9. IOT (3 experiments)</li> <li>10. My RIO(3 experiments)</li> </ol>	
<b><u>Total</u></b>		96
<b><u>Pedagogy:</u></b>	Presentation and Laboratory works	
<b><u>Learning Outcomes</u></b>	After completion of this course on practical they will be able to develop and design some applications based on SPEEDY 33 using LABView , MYRio, Altera DE2 Board	

**Course Code:** ELD 401

**Title of the Course:** PROJECT

**Number of Credits:** 8

<b><u>Prerequisites for the course:</u></b>	Decided by DC at the beginning of the IIIrd semester based on the performance at M.Sc part-I	
<b><u>Objective:</u></b>	This course develops concepts design modules/ instrumentation as required by industry/ institution/ departments	
<b><u>Content:</u></b>	This course is basically to utilize the knowledge they have acquired during the course of study and apply them for designing a gadget/interface/module required for an electronic industry/ department/ Institution. The progress of the project is periodically monitored by an guide and department council.	192
<b><u>Pedagogy:</u></b>	Self-study/presentation	
<b><u>Learning Outcomes</u></b>	<ol style="list-style-type: none"> <li>1. The Student will be exposed to the different kinds of working environments in electronic industries.</li> <li>2. Will be able to understand industrial flow and make a dissertation.</li> </ol>	

**Course Code:** ELD 481

**Title of the Course:** Swayam-IV(Wireless Sensors Network/Data Analytics Programming)

Prerequisite/objectives/learning outcomes as provided by course on SWAYAM website.

Number of Credits: 4

Course Code: ELD 402 Title of the Course: Nanoelectronics and Nanosystems

Number of Credits: 4

<b><u>Prerequisites for the course:</u></b>	The students should have a working knowledge of electronics and instrumentation at graduate level	
<b><u>Objective:</u></b>	This course develops concepts in Microelectronics, Biological Networks, Bio and Molecular Electronics and Nanoelectronics.	
<b><u>Content:</u></b>	<b>Introduction:</b> Development of microelectronics;	05
	<b>Potentials of Silicon Technology; Basics of Nanoelectronics,</b> some physical fundamentals, basics of information theory;	05
	<b>Biology Inspired Concepts.-</b> Biological networks, Biology Inspired Concepts;	05
	<b>Bio-chemical and Quantum-Mechanical Computers.-</b> DNA computer ,Quantum computer;	06
	<b>Bio-chemical and Quantum-Mechanical Computers.-</b> DNA computer ,Quantum computer;	06
	<b>Parallel Architectures for Nanosystems.</b> Architectural principles, Architectures for parallel processing;	06
	<b>Softcomputing and Nano electronics.-</b> methods of soft computing, characteristics of neural networks in nanoelectronics;	08
	<b>Quantum Electronics; Bio and Molecular Electronics.-</b> Bio electronics ,molecular electronics;	07
	<b>Nanoelectronics with Tunneling Devices;</b> Single Electron Transistor (SET); Nanoelectronics with Superconducting Devices; The Limits of Integrated Electronics	
	Tutorials: 1. Laser tweezers. 2. Study of AFM. 3. Study of STM.	
<b><u>Total</u></b>		48

<b><u>Pedagogy:</u></b>	lectures/ tutorials/assignments/self-study/presentation/	
<b><u>References/Readings</u></b>	<ol style="list-style-type: none"> <li>1. Nanoelectronics And Nanosystem By K. Goser , P Glosekotter &amp; J. Dienstuhl Springer</li> <li>2. Introduction to Nanoelectronics Science, Nanotechnology, Engineering, and Applications By Vladimir V. Mitin etal ; From Cambridge</li> <li>3. Handbook of Nanoscience, Engineering, and Technology, Second Edition by William A. Goddard CRC.</li> </ol>	
<b><u>Learning Outcomes</u></b>	At the end of this course students will be able to apply the concepts studied in this paper to practical reality .	

**Course Code:** ELD 403**Title of the Course:** Pharmaceutical Instrumentation

**Number of Credits:** 4

<b><u>Prerequisites for the course:</u></b>	Should have graduate level knowledge of Instrumentation.	
<b><u>Objective:</u></b>	This course develops concepts in Spectrometric and Separative Methods and Electron Microscopy	
<b><u>Content:</u></b>	<b>Introduction to Chemical Instrumental Analysis:</b> advantages over classical methods, classification, various units used in chemical analysis. Introduction to Electroanalytical methods, potentiometry, voltammetry, coulometry.	05
	<b>Spectrometric Methods-I:</b> Laws of Photometry, Instrument components, UV-visible instrument component, photo colorimeters, single and double beam instruments, various types of UV-visible spectrophotometers. Atomic absorption spectrophotometer: Principle, working, hollow cathode lamp, atomizer, back-ground correction.	09
	<b>Spectrometric Methods-II:</b> IR spectroscopy: Principle, IR sources, IR detectors, dispersive and Fourier , Transform IR spectroscopy. Atomic Emission Spectroscopy: Principle, types, Flame photometer, DC arc and AC arc excitation, plasma excitation. X-ray spectrometry: Instrumentation for X-ray spectrometry, X-ray diffractometer: Bragg's law	09
	<b>Spectrometric Methods-III:</b> Fluorimeters and Phosphorimeters: Principle, spectrofluorimeters, spectrophosphorimeter, Raman effect, Raman spectrometer, Nuclear Magnetic Resonance (NMR) spectrometry:	08



	Chemical shift, principle, working of NMR, FT-NMR Miscellaneous Instruments: Gas analysers: CO, CO <sub>2</sub> , Hydrocarbons, O <sub>2</sub> , NO <sub>x</sub>	07
	<b>Separative Methods:</b> Chromatography: Classification, Gas chromatography: principle, constructional details, GC detectors, High Performance Liquid Chromatography (HPLC): principle, constructional details, HPLC detectors	10
	<b>Electron microscopy:</b> TEM & SEM- principles, instrumentation and analysis, scanning tunneling microscopy, atomic force microscopy, principles, instrumentation and analysis- applications	
	<b>Tutorial:</b> 1. Study of filter photometer. 2. Study of UV-visible spectrophotometer. 3. Study of ESR	
<b>Total</b>		48
<b>Pedagogy:</b>	lectures/ tutorials/assignments/presentation	
<b>References/Readings</b>	<ol style="list-style-type: none"> <li>1. Instrumental Methods of Analysis, Willard, Merritt, Dean, Settle, CBS Publishers &amp; Distributors, New Delhi, Seventh edition.</li> <li>2. Instrumental Methods of Chemical Analysis, Galen W. Ewing, McGraw-Hill Book Company, Fifth edition</li> <li>3. Introduction to Instrumental Analysis, Robert D. Braun, McGraw-Hill Book Company.</li> <li>4. Principles of Instrumental Analysis, Skoog, Holler, Nieman, Thomson brooks-cole publications, 5<sup>th</sup> edition</li> </ol>	
<b>Learning Outcomes</b>	A student crediting this course will be comfortable with use of analytical instruments used in pharmaceutical industries and laboratories. They can join industries in Quality Control divisions.	

**Course Code:** ELD 404

**Title of the Course:** Communication and Technical Skills

**Number of Credits: 4**

<b><u>Prerequisites for the course:</u></b>	Should have graduation in any science stream	
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<b><u>Objective:</u></b>	This course develops the ability to work in group, to face interviews and to give presentations	
<b><u>Content:</u></b>	<p>This course has self-study module where students will be assigned case studies. The students are supposed to gather the required subject materials by way of visiting the factories/ industries/ Institutions physically or through their website, and prepare a documentation, The documentation will be discussed in Group discussion wherein the skills of the student in Management &amp; Communication will be evaluated by DC.</p> <p>The wattage of the evaluation is as follows</p> <ul style="list-style-type: none"> <li>• Group discussion in topic related to electronics (25%)</li> <li>• Answer paper in the area of communication skills (25%)</li> <li>• Has to write /compile technical papers &amp; present (25%)</li> <li>• Modelling of electronics systems (25%)</li> </ul>	
<b><u>Pedagogy:</u></b>	lectures/ tutorials/assignments/self-study	
<b><u>References/Readings</u></b>	<ol style="list-style-type: none"> <li>1. Essentials of Technical Communication Sunil Gokhale</li> <li>2. Communication Skills By Leena, Sen, Prentice Hall of India.</li> <li>3. <a href="http://owl.english.purdue.edu/">http://owl.english.purdue.edu/</a>;</li> <li>4. <a href="http://owl.english.purdue.edu/workshops/hypertext/">http://owl.english.purdue.edu/workshops/hypertext/</a></li> </ol>	
<b><u>Learning Outcomes</u></b>	The student will gain experience and confidence to present themselves fearlessly at interviews .The students will also be prepared to write technical papers and present them in the conferences.	

## M.Sc. Electronics Ranking Test Syllabus

Max. Marks: 100

(50 objective questions of 2 marks each)

Questions will be categories in 3 different level

Level 1(15 General questions of simpler nature)Level 2( 15 General question of Moderate Level)

Level 3(20 questions of High Level)

**Note :** Each correct response will give 2 Marks and each wrongresponse has -0.5

**Physics of Solid State Devices:** Semiconductor, metal and insulator, electrical characteristics and energy band structure, Extrinsic and intrinsic semiconductors, FD statistics and carrier concentration, Diffusion and drift of carriers, I-V characteristics of junction diodes, BJT, FET, MOS I-V relationship, Small signal equivalent circuits, Photodiodes, Light emitting diodes.

**Mathematics and Computer programming:** First and Second Order differential equations, Basic matrix algebra, elementary statistics, elementary C programming.

**General Knowledge on science and technology**, household electrical gadgets (Fan, Household electrical wiring, Electric iron, Electric lamp, etc.), Energy conservation, environment pollution, scientific organizations in Goa, Major Indian research organizations/industries in Power, Computer Software, Textile, Consumer Electronics, Atomic Energy, Communication Engineering, Pharmaceuticals, Names of Inventors and Pioneers in Physics, Chemistry, Mathematics, Statistics, Electronics, Economics, Engineering, Oceanography Botany.

**General Aptitude**

**Networks and Circuit Theory**: Nodal and Mesh Analysis, Thevenin's and Norton's Theorem, Maximum Power Transfer theorem, Sinusoidal steady state analysis, Time Domain analysis of simple RLC circuits.

**Analog Electronics**: Basic rectifier, clipping and clamping circuits, single stage CE amplifier (Gain and frequency response), Emitter follower, Differential amplifier, Op-amp specifications and simple applications (Inverting and Non-Inverting amplifier, differentiator, integrator, V-I converter), Power Amplifier (class A, B)

**Digital Electronics**: Boolean Algebra and truth table, Basic gates (AND, OR, NAND, NOR, etc.), number system (Decimal, Hexadecimal, Octal), TTL gates (specifications), SSI, MSI, VLSI, complexity, flip-flop and counters, RAM, ROM (basic concepts), ALU, elements of 8085 microprocessor, assembly language programming.

**Communication Theory and practice**: Elements of Vector calculus, gradient, divergence and curl, Maxwell's equations, Fourier analysis of signals, amplitude and frequency modulation, block diagram of radio receiver.

1. **Instrumentation**: Multimeter, CRO, Power supply.

**Books recommended:**

1. Network Lines and Fields, J. D. Ryder, Prentice Hall India.
2. Network Analysis, Van Vankelburg, Prentice Hall India.
3. Electronics Devices and Circuits, Millman and Halkias, Tata McGraw Hill.
4. Communication Engineering, Shrader, McGraw Hill.
5. Introduction to Electrodynamics, David J. Griffiths.
6. Introduction to Mathematical Physics, Charlie Harper.
7. Programming with C, Bryon Gottfried, Schaum Series
8. Digital Principles and Applications, Malvino and Leach, McGraw Hill.
9. Microprocessor Architecture, Programming and Applications with 8085, Ramesh Gaonkar, Pernem International.
10. Communication Systems, Simon Haykins, Wiley
11. Competition Success Reviews and Competition Refresher